

DOSSIER-Cloud DevOpS-based Software engIneERing for the cloud





# Πρόσκληση

1° μινι-σχολείο στο Υπολογιστικό Νέφος και τις Υπηρεσίες Λογισμικού

### Πέμπτη, 15 Σεπτεμβρίου 2016

Αίθουσα Συνεδριάσεων, Ισόγειο Υπηρεσίας Σπουδών και Φοιτητικής Μέριμνας, Γωνία Αθηνών και Ν. Ξιούτα, Λεμεσός

### Παρασκευή, 16 Σεπτεμβρίου 2016

Πολυχώρος ΚΤΗΣΙΣ, Πλατεία Ηρώων, Λεμεσός Το Τμήμα Ηλεκτρολόγων Μηχανικών, Μηχανικών Η/Υ και Πληροφορικής του Τεχνολογικού Πανεπιστημίου Κύπρου, και οι εταίροι του έργου Dossier-Cloud που χρηματοδοτείται από την ΕΕ και το πρόγραμμα HORIZON2020, σας προσκαλούν στο πρώτο μινι-σχολείο με θέματα γύρω από το Υπολογιστικό Νέφος και τις Υπηρεσίες Λογισμικού. Στο διήμερο σχολείο θα μιλήσουν διακεκριμένοι καθηγητές και ερευνητές από το Πολυτεχνείο του Μιλάνου (Politecnico di Milano - POLIMI). Οι διαλέξεις θα είναι στα Αγγλικά.



Πληροφορίες: 25 00 25 33 http://www.dossier-cloud.eu





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# Invitation

1<sup>st</sup> mini-school on Cloud Computing and Software Services in the context of the Dossier-Cloud project

### Thursday, September 15, 2016

Student Services Building Conference Room, Ground Floor, Athinon and N.Xiouta corner, Lemesos

### Friday, September 16, 2016

Polichoros Ktisis, Heroes Square, Limassol The Department of Electrical Engineering, Computer Engineering and Informatics of the Cyprus University of Technology, and the consortium of the EU funded project Dossier-Cloud (HORIZON-2020) cordially invite you to attend the 1<sup>st</sup> mini-school on Cloud Computing and Software Services. During this two-day event renowned professors and researchers from the Politecnico di Milano (POLIMI), Italy, will deliver lectures on topics from the area of Cloud Computing and Software Services. The lectures will be in English.



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# Πρόγραμμα / Program

Day 1 - Thursday, 15/9/2016	09:00 - 09:15 - Welcoming. Introduction to Dossier-Cloud project
	(A. Christoforou, A.S. Andreou)
	09:15 - 10:15 – Monitoring of Cloud-/Service-based Systems
	(Luciano Baresi)
	10:15 - 10:45 – Questions/Discussion on previous talk (audience)
	10:45 - 11:15 – Coffee break
	11:15 - 12:15 – Hybrid Cloud-based Applications (Luciano Baresi)
	12:15 - 12:45 – Questions/Discussion on previous talk (audience)
	12:45 - 13:45 - Cloud-based Software Verification (Srdjan Krstić)
	13:45 - 14:15 – Questions/Discussion on previous talk (audience)
	14:15 - 15:30 – Lunch break
Day 2 - Friday, 16/9/2016	09:15 - 10:15 – Dependability of Adaptable and Evolvable
	Distributed and Multi-Owner Systems – Part A' (Carlo Ghezzi)
	10:15 - 10:45 – Questions/Discussion on previous talk (audience)
	10:45 - 11:15 – Coffee break
	11:15 - 12:15 – Dependability of Adaptable and Evolvable
	Distributed and Multi-Owner Systems – Part B' (Carlo Ghezzi)
	12:45 - 13:45 – Specification of Cloud-Based Elastic Systems
	(Srdjan Krstić)
	13:45 - 14:15 – Questions/Discussion on previous talk (audience)

## Short CVs

#### **Carlo Ghezzi**

Carlo Ghezzi is a Professor and Chair of Software Engineering at the Politecnico di Milano - Dipartimento di Elettronica, Informazione e Bioingegneria. He has been the Rector's delegate for research, past member of the Academic Senate and of the Board of Governors, and past Department Chair. He held temporary or visiting positions at the University of California at Los Angeles, University of North Carolina at Chapel Hill, University of Padova, ESLAI-Argentina, University of California at Santa Barbara, Technical University of Vienna, University of Klagenfurt. He has an Adjunct Professor position at the Faculty of Informatics of the University of Lugano.

He is an ACM Fellow, IEEE Fellow, Member of Academia Europaea, Member of the Italian Academy of Sciences (Istituto Lombardo). He has been awarded the 2006 ACM SIGSOFT Distinguished Service Award. He is past President of Informatics Europe. He has been on the evaluation board of several international research projects and institutions in Europe, Japan, and the USA.

He is a regular member of the program committee of important conferences in the software engineering field, such as the ICSE and ESEC/FSE, for which I also served as Program and General Chair. I have given keynotes at several international conferences, including ESEC/FSE and ICSE. I have been the Editor in Chief of the ACM Trans. on Software Engineering and Methodology (from 2001 till 2006). He is currently an Associate Editor of Communications of the ACM, Science of Computer Programming, Service Oriented Computing and Applications, and Computing.

His research has been focusing on software engineering and programming languages. Currently, he is especially interested in methods and tools to improve dependability of adaptable and evolvable distributed applications, such as service-oriented architectures and ubiquitous/pervasive computer applications. He co-authored over 200 papers and 8 books. He coordinated several national and international (EU funded) research projects and I have been awarded an Advanced Grant from the European Research Council.

## Short CVs

#### Luciano Baresi

Luciano Baresi is a full professor at the Politecnico di Milano -Dipartimento di Elettronica, Informazione e Bioingegneria, where he earned both his laurea (master) degree and PhD in computer science. Luciano was visiting professor at University of Oregon (USA) and visiting researcher at University of Paderborn (Germany).

Luciano was program chair for ICECCS02, FASE06, ICWE07, ICSOC09, SEAMS12 and ESEC/FSE13, and general chair for WICSA/CompArch16. Luciano is member of the editorial board of ACM Transactions on Autonomous and Adaptive Systems, IEEE Transactions on Software Engineering, and of IEEE Transactions on Service Computing.

Luciano has co-authored more than 130 papers. Some of his works appeared on top-level journals/magazines like ACM Transactions on Software Engineering and Methodology, IEEE Transactions on Software Engineering, IEEE Computer, IEEE Software, and IEEE Internet Computing. He is also co-author of a book in Italian. Currently his work has been cited some 6500 times and his h-index is 40 (source Google Scholar). Luciano was awarded two CNR short-term mobility grants, an IBM Shared University Research award and an IBM Faculty award.

His research interests are in the broad area of software engineering. At the beginning he was interested in formal approaches for modeling and specification languages, he then moved to UML and the design of Web applications. Currently, he is interested in distributed systems, service-based applications and in the different aspects of mobile, self-adaptive, and pervasive software systems. His research has always been funded by participating in national and international projects. Among these, Luciano was responsible of the activities at Politecnico for the following EU-supported projects: MOMOCS, SLA@SOI, MADES, and Indenica.

## Short CVs

#### Srdjan Krstic

Srdjan received his BSc in Electrical engineering and Computer Science from the University of Niš, Faculty of Electronic Engineering in 2010, his MSc in Engineering of Computing systems in 2012, and his PhD in Information Technologies in 2016, both from the Politecnico di Milano, Dipartimento di Elettronica, Informazione e Bioingegneria (DEIB).

He is currently a Postdoctoral Fellow with the Politecnico di Milano. He was also Visiting Scientist at NASA Ames Intelligent Systems Division where he collaborated with researchers from the robust software engineering group to investigate runtime monitoring of requirements in the context of Unmanned Aircraft Systems (UASs) and air-traffic control systems. He served as Teaching Assistant at the Politecnico di Milano.

The main focus of his research is on the specification and verification of quantitative properties of complex systems. He has a strong formal mathematical background and experience in specifying system properties using different specification languages based on temporal logic. He has developed decision procedures for such specification languages and showed how they can be applied to trace checking and runtime verification. Finally, he has studied distributed computational frameworks like MapReduce and Spark to scale-up and parallelize the above-mentioned verification procedures.